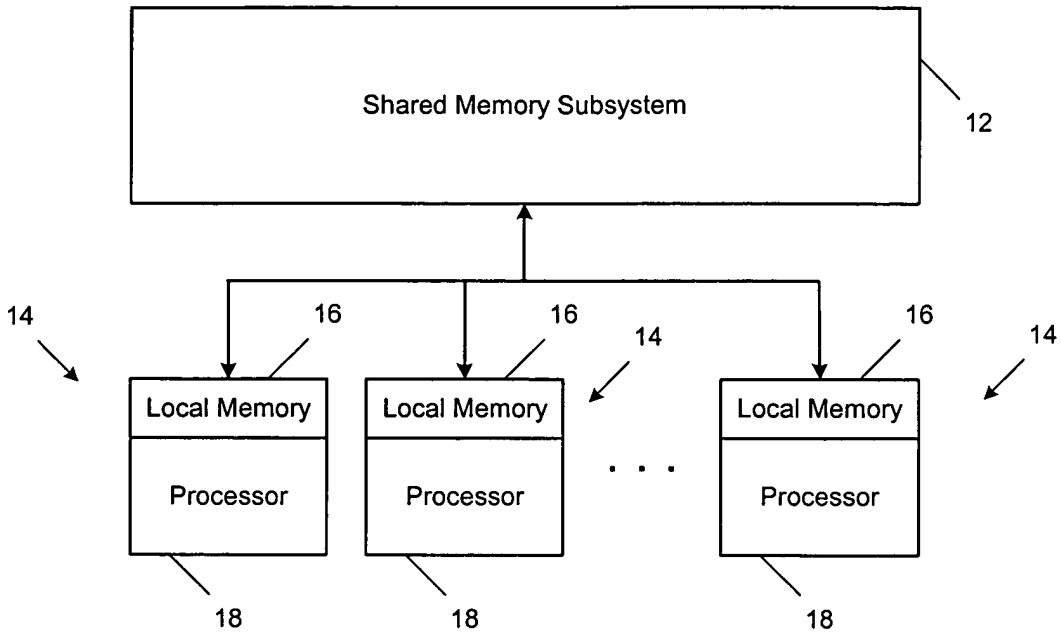

FIG. 1 (PRIOR ART)

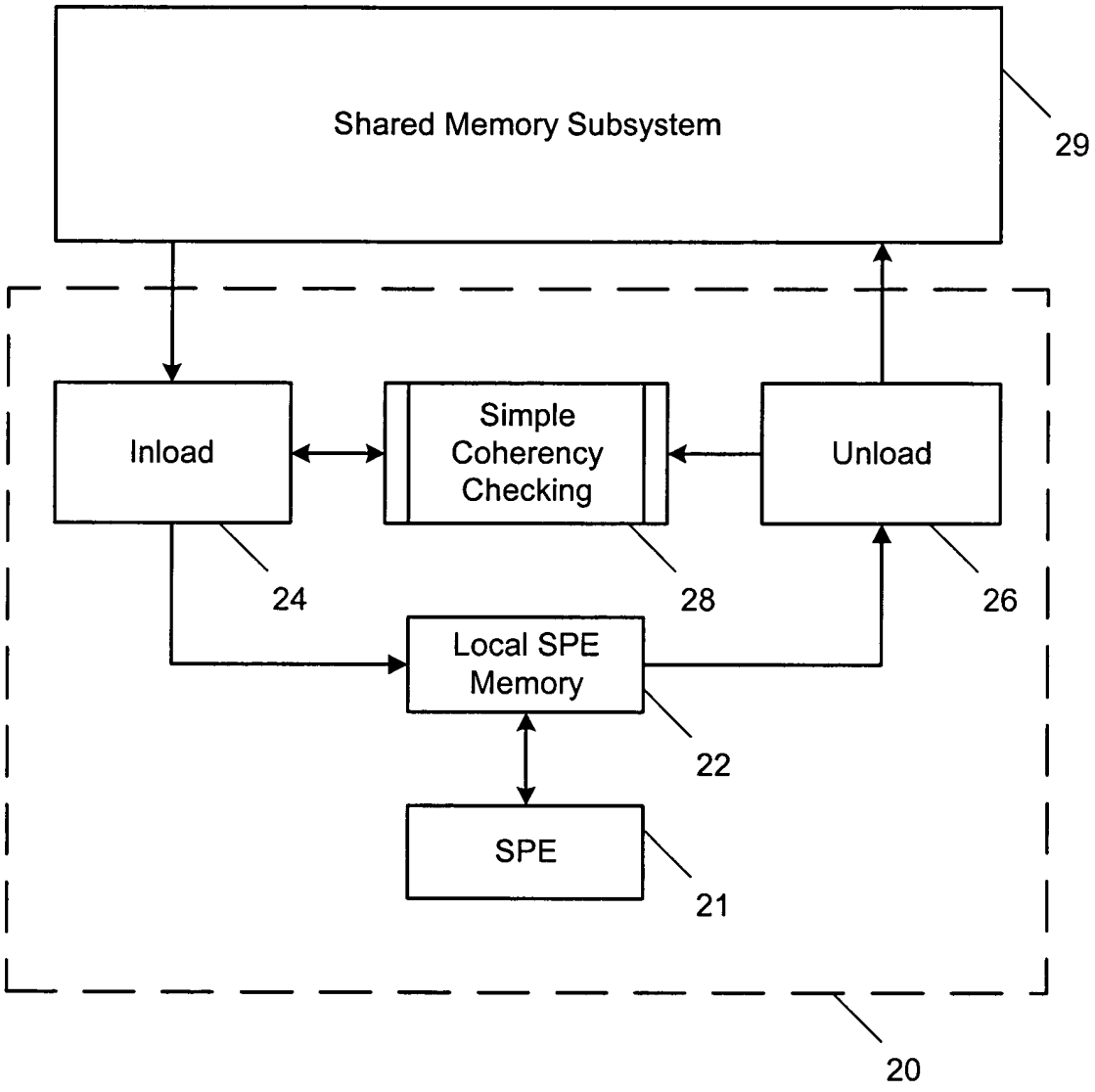
Shared Memory Subsystem
29
Inload
Simple Coherency Checking
Unload
24
28
26
Local SPE Memory
22
SPE
21
20

FIG. 2

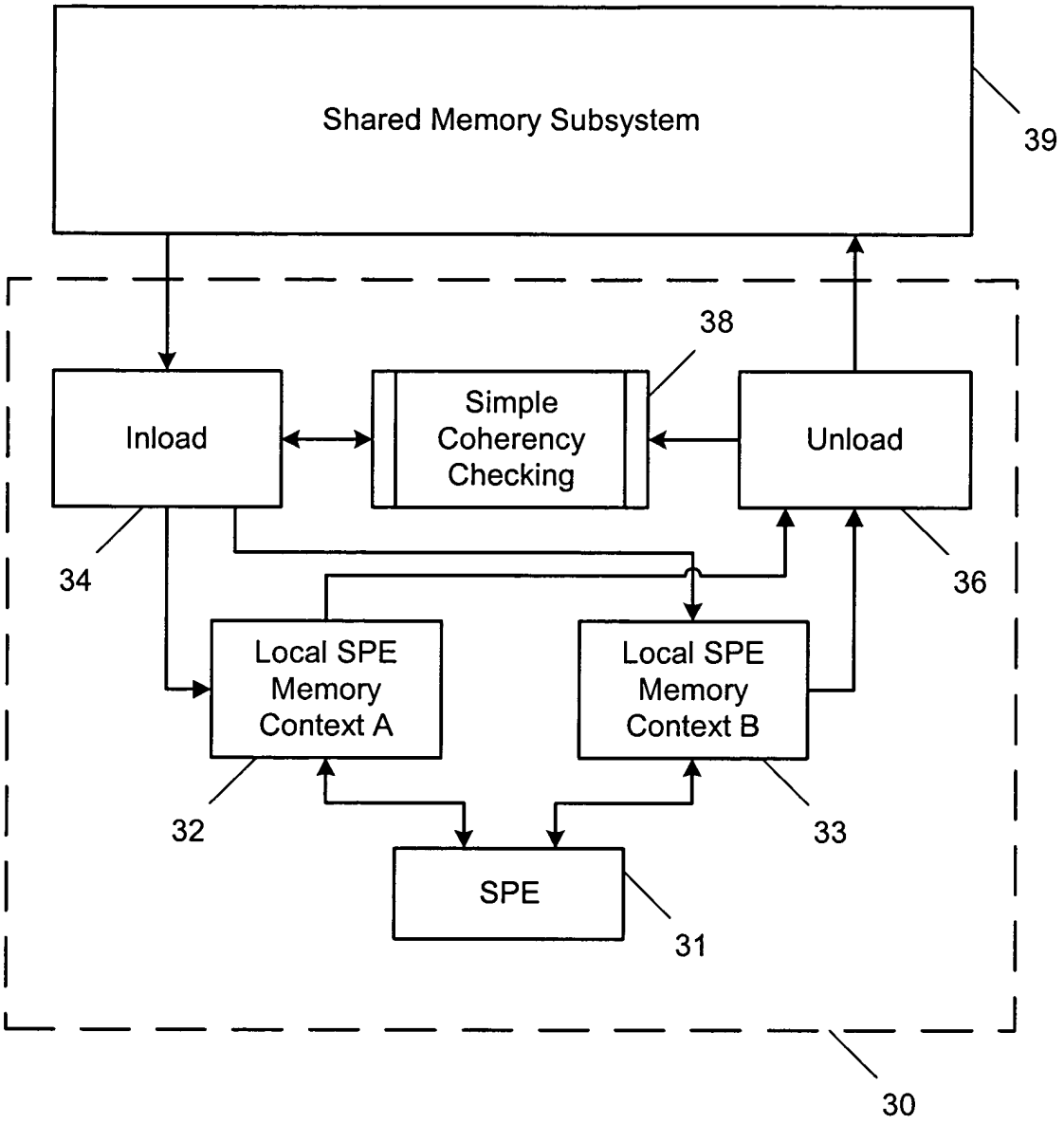
Shared Memory Subsystem
39
38
Inload
Simple Coherency Checking
Unload
34
36
Local SPE Memory Context A
Local SPE Memory Context B
32
33
SPE
31
30

FIG. 3

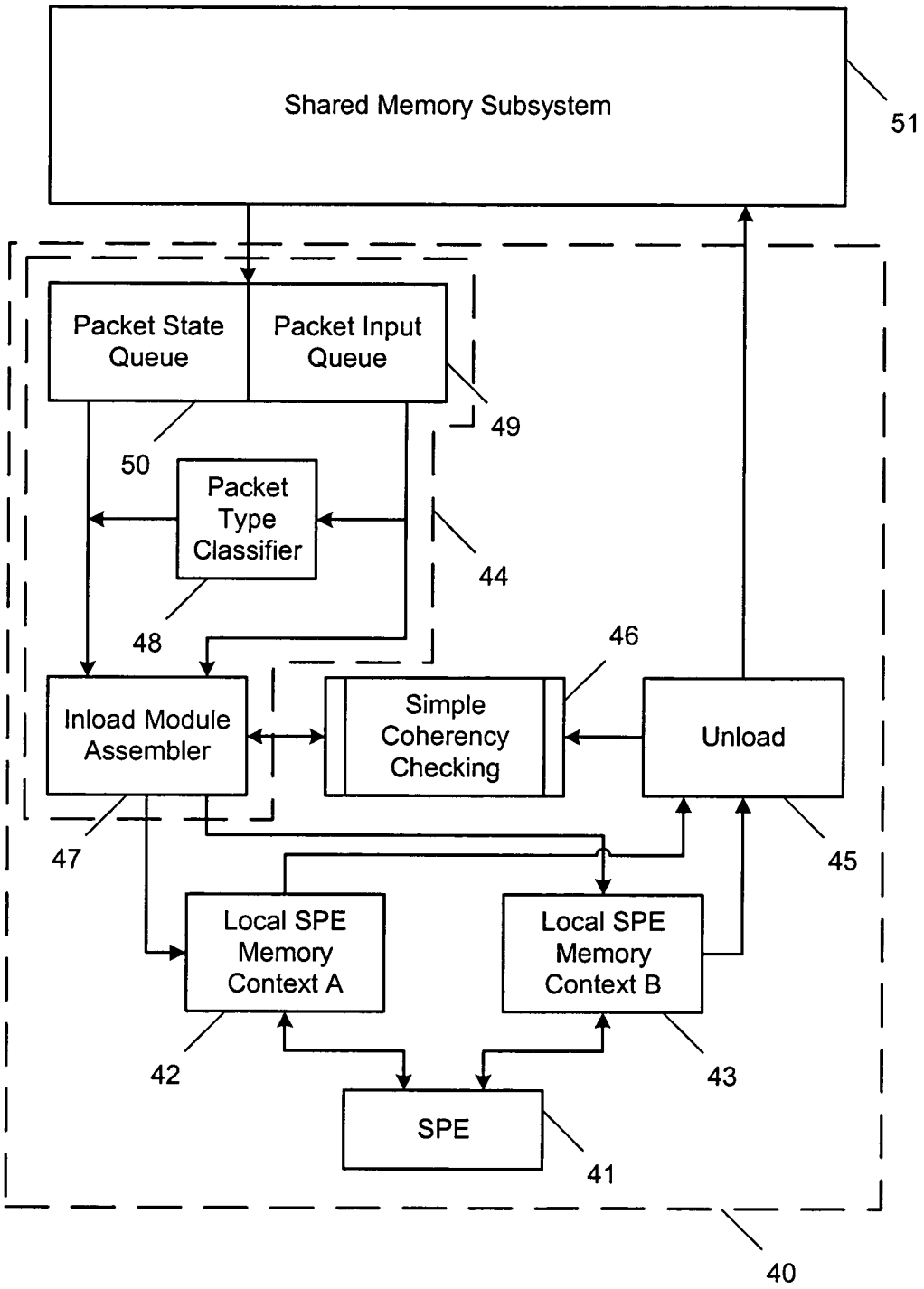
Shared Memory Subsystem
51
Packet State Queue
Packet Input Queue
49
50
Packet Type Classifier
44
48
46
Inload Module Assembler
Simple Coherency Checking
Unload
47
45
Local SPE Memory Context A
Local SPE Memory Context B
42
43
SPE
41
40

FIG. 4

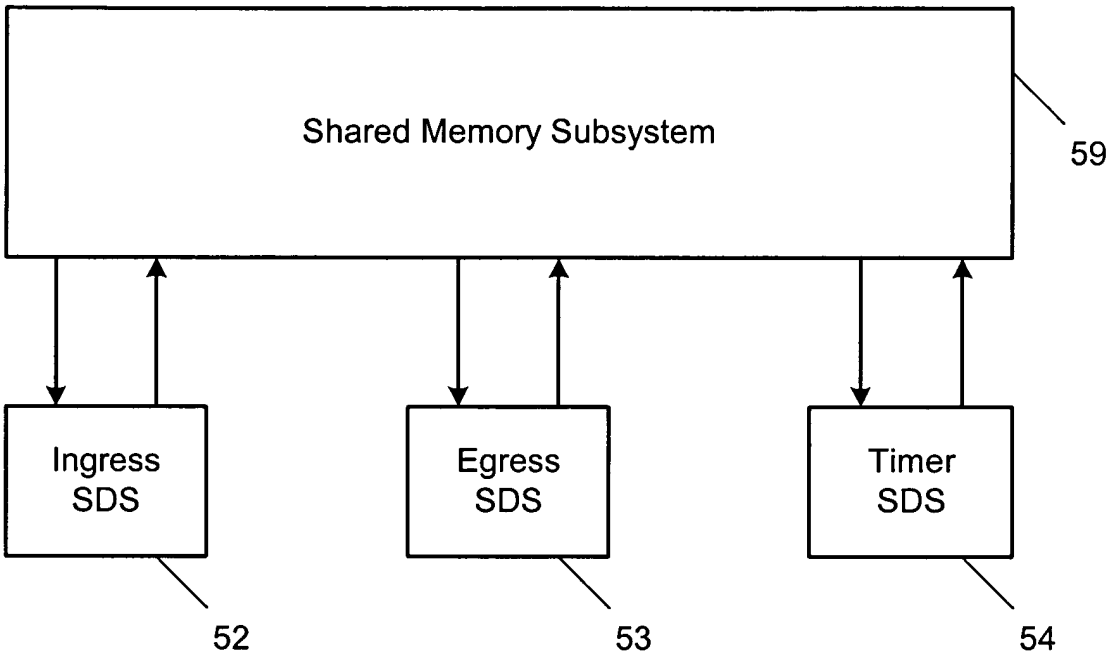
Shared Memory Subsystem
59
Ingress SDS
Egress SDS
Timer SDS
52
53
54

FIG. 5

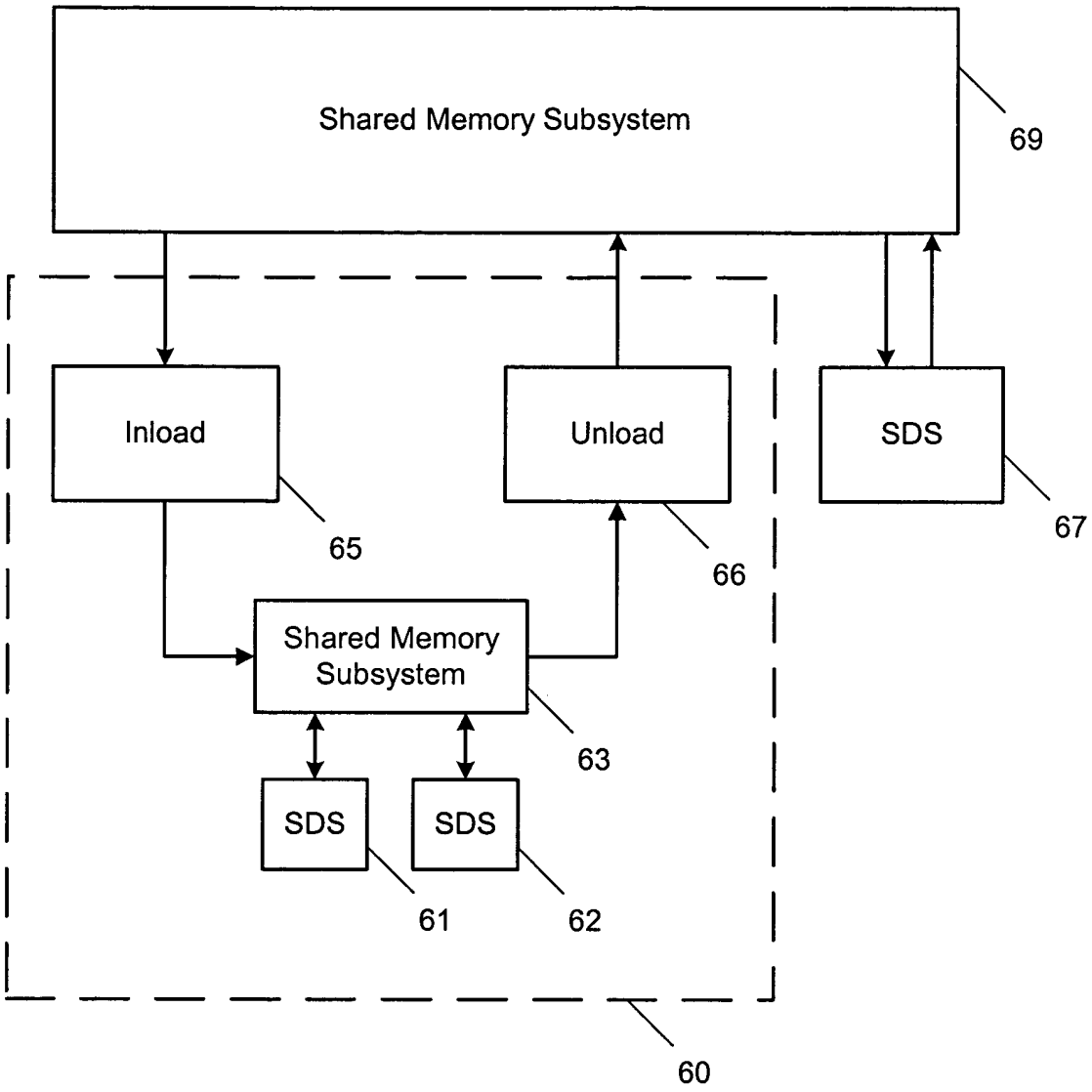
Shared Memory Subsystem
69
Inload
Unload
SDS
65
66
67
Shared Memory Subsystem
63
SDS
SDS
61
62
60

FIG. 6